ABSTRACT OF THE DISCLOSURE

A bus bridge circuit adapted for coupling to a bus comprising *n* address lines, where *n* is an integer and $n \geq 2$. The bus bridge circuit includes audio logic configured to access digital audio data and to produce an *n*-1 bit address when accessing the digital audio data, and an addressable register including a bit position for storing an additional address bit. When the audio logic is accessing digital audio data, the bus bridge circuit is configured to: (i) concatenate the additional address bit with the *n*-1 bit address to produce an *n*-bit address, wherein the additional address bit forms a most significant bit of the *n*-bit address, and (ii) drive the *n*-bit address upon the *n* address lines of the bus. A computer system is described including the bus, wherein the bus bridge circuit is coupled to the bus. A method for initializing a chip set of a computer system is also described, wherein the chip set comprises the bus bridge circuit coupled to the bus. A carrier medium embodying program instructions for carrying out the above method is also described. The carrier medium may be, for example, a computer-readable storage medium (e.g., a floppy disk, or a compact disk read only memory or CD-ROM disk).